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			Application Number	09/961,114
TRANSMITTAL		Filing Date	Şep 20, 2001	
FORM			First Named Inventor	Perino et al.
(to be used for all correspondence after initial filing)		Group Art Unit	2833	
		Examiner Name	Paumen	
Total Number of Pages in This Submission 16		on 16	Attorney Docket Number	RB1-035USC3
		ENCL	OSURES (check	all that apply) After Allowance Communication
Fee Transmittal Form Fee Attached Amendment / Reply After Final Affidavits/decla Extension of Time Rec Express Abandonmer Information Disclosure Certified Copy of Prior Document(s) Response to Missing Incomplete Applicatio Response to M under 37 CFR	quest Int Request E Statement Inty Parts/ In Alissing Parts 1.52 or 1.53	CD, N	ng-related Papers n to Convert to a conal Application of Attorney, Revocation e of Correspondence sal Disclaimer st for Refund umber of CD(s)	to Group Appeal Communication to Board of Appeals and Interferences Appeal Communication to Group (Appeal Notice, Brief, Reply Brief) Proprietary Information Status Letter Other Enclosure(s) (please identify below):
	SIGNATUR	E OF APPL	LICANT, ATTORNEY, OF	RAGENT
Firm or Individual name	Daniel L. Hayes			
Signature	Daniel.	1.7tz	my MS	
Date	12/10/0		·	
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Typed or printed name

LeAnn M. Sassman

Signature

LeAnn M. Sassman

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Burden Hour Statement: This form is estimated to take 0.2 hours to complete. Time will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, Washington, DC 20231. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Assistant Commissioner for Patents, Washington, DC 20231.

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

	09/961 114
Application Serial No	09/20/2001
Inventor	Rambus, Inc.
Applicant	2833
Applicant	
Attorney's Docket No. Title: Chip Socket Assembly and Chip File Assembly fo	r Semiconductor Chips
Title: Chip Socket Assembly and Chip File Assembly to	1 Dollingoniant of the

SUBSTITUTE DRAWING REQUEST

To:

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Drawing Review Branch

Commissioner of Patents and Trademarks,

Washington, D.C. 20231

From:

Daniel L. Hayes (Tel. 509-324-9256; Fax 509-323-8979)

Customer No. 29150



29150

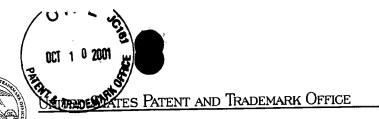
Applicant submits herewith a new set of formal drawings to correct informalities noted by the official draftsperson in Notice to File Corrected Application Papers.

Respectfully Submitted,

Date: 12/10/01

Daniel L. Hayes

Reg. No. 34,618 (509) 324-9256



EL887746002

COMMISSIONER FOR PATENTS
UNITED STATES PATENT AND TRADEMARK OFFICE
WASHINGTON, D.C. 20231
www.uspto.gov

APPLICATION NUMBER

LEE & HAYES, PLLC

SPOKANE, WA 99201

29150

FILING/RECEIPT DATE

FIRST NAMED APPLICANT

ATTORNEY DOCKET NUMBER

09/961,114

421 W. RIVERSIDE AVE, STE 500

09/20/2001

Donald V. Perino

RB1-035USC3

CONFIRMATION NO. 4507

FORMALITIES LETTER

OC000000006900423

Date Mailed: 10/15/2001

NOTICE TO FILE CORRECTED APPLICATION PAPERS

Filing Date Granted

This application has been accorded an Application Number and Filing Date. The application, however, is informal since it does not comply with the regulations for the reason(s) indicated below. Applicant is given **TWO MONTHS** from the date of this Notice within which to correct the informalities indicated below. Extensions of time may be obtained by filing a petition accompanied by the extension fee under the provisions of 37 CFR 1.136(a)

The required item(s) identified below must be timely submitted to avoid abandonment:

- Substitute drawings in compliance with 37 CFR 1.84 because:
 - drawing sheets do not have the appropriate margin(s) (see 37 CFR 1.84(g)). Each sheet must include a top margin of at least 2.5 cm. (1 inch), a left side margin of at least 2.5 cm. (1 inch), a right side margin of at least 1.5 cm. (5/8 inch), and a bottom margin of at least 1.0 cm. (3/8 inch);

A copy of this notice <u>MUST</u> be returned with the reply.

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